IN THE SPECIFICATION:

Please Amend the ABSTRACT as follows:

Approved
04/22/08
T.H.

A system for transmitting subchannel data over the same media as high speed data, especially DC balanced high speed data. A digital transmitter receives subchannel data and modulates it onto a subchannel carrier as the host signal in the preferred embodiment using frequency shift keying. The FSK subchannel carrier is added by superposition to the high speed data signal. ~~At the receiver, a low pass filter filters out the subchannel carrier frequency components which are then amplified and subjected to a two-pole anti-aliasing filter to remove the third and fifth harmonics. Analog-to-digital conversion is followed by a digital mix down to remove the subchannel carrier component using a local oscillator at the subchannel carrier frequency. An FIR low pass filter having a triangular time domain response performs anti-aliasing and leaves as an output only frequency components at the positive and negative deviation frequency for Mark and Space. Mark and space detectors then determine the relative magnitude of the signal received at the positive and negative deviation frequencies in the complex phasor domain to determine when Mark and Space frequencies are being received.~~ Because of the ability to have very narrow bandwidths for the passbands of ~~the~~ digital IF filters, the receiver is more sensitive and less susceptible to the need for tuning to ~~accomodate~~ accommodate manufacturing variations in the rise times of the high speed data signals.

Please Amend paragraph [0001] as follows:

[0001] This is a continuation of U.S. patent 6,693,917, filed August 27, 1999 entitled DIGITAL SUBCHANNEL TRANSCEIVER FOR TRANSMITTING DATA which is a continuation-in-part of ~~prior~~ U.S. patent ~~application Ser. No. 09/063,633~~ 6,295,272, filed Apr. 20, 1998 entitled SUBCHANNEL MODULATION SCHEME FOR CARRYING MANAGEMENT AND CONTROL DATA OUTSIDE THE REGULAR DATA CHANNEL, which ~~is~~ are hereby incorporated by reference.